

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicants: K. KATAYAMA, et al.

Application No.: Rule 1.53(b) Continuation of U.S. Patent Application  
Serial No. 10/373,872, filed February 27, 2003

Filed: On even date herewith

For: SEMICONDUCTOR MEMORY DEVICE  
HAVING FAULTY CELLS

Art Unit in parent: 2818

Examiner in parent: T. Q. Phan

**PRELIMINARY AMENDMENT**

Mail Stop  
Commissioner for Patents  
P.O. BOX 1450  
Alexandria, VA 22313-1450

July 30, 2003

Sir:

Prior to examination, please amend the above-identified application as listed below and as set forth on the following pages:

**Amendments to the Specification**

**Remarks** are included following the amendments.